2.4　Fast Electronics

The fast electronics group at JLab is capable of contributing to several electronics projects for GlueX. The three main tasks that should be worked on

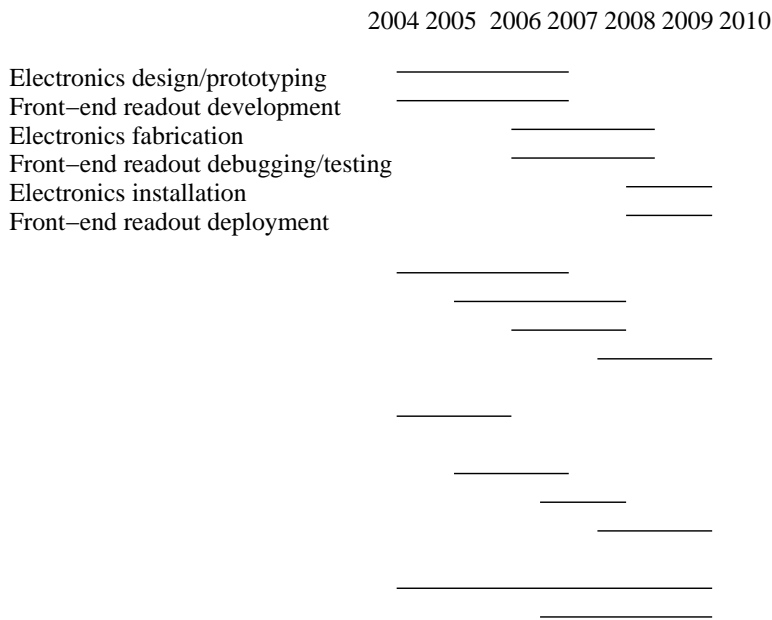and software, as well as to the signal distribution across electronic crates.

2004 2005 2006 2007 2008 2009 2010

Electronics design/prototyping
Front–end readout development
Electronics fabrication
Front–end readout debugging/testing
Electronics installation
Front–end readout deployment

Figure 4: A time line showing the overall schedule for the GlueX DAQ e ort.

GlueX Computing Center   Timeline

2004 2005 2006 2007 2008 2009 2010

GRID activities

Computation

Data Storage
Raw/Simulated
Reconstructed

Offline computing
Reconstruction

Infrastructure
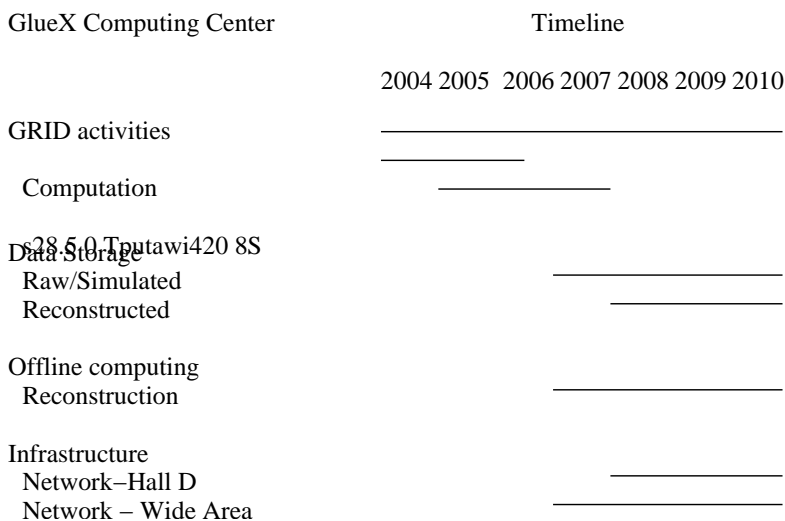Network–Hall D
Network – Wide Area

Figure 5: A time line showing the timeline for various activities by the computing center in support of GlueX.

2.7 Software Deliverables and Support for GlueX

The primary responsibilities of JLab to the software e ort will be to provide

the DAQ system, described above, followed by integration provided by Hall

specifications, time schedules and costs cannot be met by the Je erson Lab group.

2 The GlueX collaboration retains the right to assign additional manpower and/or additional groups to this project if it is deemed that this is necessary for timely and within budget completion of the project.

3 The continuation of this agreement is dependent on the approval for continuing funding for all parties in the MOU.

4 This agreement may be amended as necessary.

5 The Je erson Lab group, the GlueX Collaboration management and the JLab management of GlueX agree to commit themselves on a collegial, open and e ective working relationship for the benefit of the project.